

## Refine Search

### Search Results -

Terms	Documents
(semiconductor and (insulat\$ same ((air adj gap) or empty or void))).clms.	0

Database:

**US Pre-Grant Publication Full-Text Database**

 US Patents Full-Text Database  
 US OCR Full-Text Database  
 EPO Abstracts Database  
 JPO Abstracts Database  
 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

Search:

L20





### Search History

 DATE: Wednesday, December 21, 2005    [Printable Copy](#)    [Create Case](#)

<u>Set Name</u> side by side	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u> result set
	<i>DB=PGPB; PLUR=YES; OP=ADJ</i>		
<u>L20</u>	(semiconductor and (insulat\$ same ((air adj gap) or empty or void))).clms.	0	<u>L20</u>
	<i>DB=JPAB; PLUR=YES; OP=ADJ</i>		
<u>L19</u>	L6 and L12	0	<u>L19</u>
	<i>DB=EPAB; PLUR=YES; OP=ADJ</i>		
<u>L18</u>	L5 and L11	5	<u>L18</u>
	<i>DB=USPT; PLUR=YES; OP=ADJ</i>		
<u>L17</u>	L4 and L10	570	<u>L17</u>
	<i>DB=PGPB; PLUR=YES; OP=ADJ</i>		
<u>L16</u>	L3 and L9	450	<u>L16</u>
	<i>DB=TDBD; PLUR=YES; OP=ADJ</i>		
<u>L15</u>	L2 and L8	8	<u>L15</u>
	<i>DB=DWPI; PLUR=YES; OP=ADJ</i>		
<u>L14</u>	L2 and L8	0	<u>L14</u>
<u>L13</u>	L1 and L7	117	<u>L13</u>

*DB=JPAB; PLUR=YES; OP=ADJ*

L12    L11

0    L12

*DB=EPAB; PLUR=YES; OP=ADJ*

L11    semiconductor and (insulat\$ same ((air adj gap) or empty or void))

41    L11

*DB=USPT; PLUR=YES; OP=ADJ*

L10    semiconductor and (insulat\$ same ((air adj gap) or empty or void))

4308    L10

*DB=PGPB; PLUR=YES; OP=ADJ*

L9    semiconductor and (insulat\$ same ((air adj gap) or empty or void))

2351    L9

*DB=TDDB; PLUR=YES; OP=ADJ*

L8    semiconductor and (insulat\$ same ((air adj gap) or empty or void))

24    L8

*DB=DWPI; PLUR=YES; OP=ADJ*

L7    semiconductor and (insulat\$ same ((air adj gap) or empty or void))

885    L7

*DB=JPAB; PLUR=YES; OP=ADJ*

L6    semiconductor and (trench same ((air adj gap) or empty or void))

99    L6

*DB=EPAB; PLUR=YES; OP=ADJ*

L5    semiconductor and (trench same ((air adj gap) or empty or void))

13    L5

*DB=USPT; PLUR=YES; OP=ADJ*

L4    semiconductor and (trench same ((air adj gap) or empty or void))

1764    L4

*DB=PGPB; PLUR=YES; OP=ADJ*

L3    semiconductor and (trench same ((air adj gap) or empty or void))

1343    L3

*DB=TDDB; PLUR=YES; OP=ADJ*

L2    semiconductor and (trench same ((air adj gap) or empty or void))

15    L2

*DB=DWPI; PLUR=YES; OP=ADJ*

L1    semiconductor and (trench same ((air adj gap) or empty or void))

440    L1

END OF SEARCH HISTORY